Design Of Condition Monitoring Module

S.Arunprathap¹, R.Abinesh², S.Guruprakash³, T.Vignesh⁴

 Assistant Professor¹, Department of Electronics and Communication Engineering, M. Kumarasamy College of Engineering, Karur, Tamil Nadu, India.
UG Scholar^{2,3,4}, Department of Electronics and Communication Engineering, M. Kumarasamy College of Engineering, Karur, Tamil Nadu, India.

Abstract

The design of the Condition Monitoring module is based on the need for predictive maintenance, which necessitates a routine or permanent recording of the system condition. To control various sensors and devices, the design includes inputs such as RS-232, RS-485, IO-Link, and resistance temperature detectors (RTds). The primary aim of condition monitoring is to detect a fault, or a deterioration process, that has advanced to a certain symptomatic level, and to provide an early warning of the abnormality before it leads to a functional breakdown. As a result, we're working on a condition monitoring module to identify and correct the malfunction. The primary aim of condition monitoring is to increase plant production capability and profitability. The circuits can be implemented in Printed Circuit Boards using xpedition, and design is used very effectively.

1. Introduction

Printed circuit boards (PCBs) are the boards that function the inspiration for many electronic devices, serving as both a physical support piece and a wiring area for surface-mounted and socketed components. Fiberglass, composite epoxy, or another material are the foremost common materials wont to produce PCBs. the bulk of PCBs for basic electronics are single- layer and straightforward. Hardware with several layers, like special effects cards or motherboards, can have up to 12 layers. Despite the very fact that PCBs are most generally related to computers, they will even be utilized in TVs, radios, digital cameras, and mobile phones. Different sorts of PCBs are utilized in variety of other areas additionally to consumer electronics and computers. Multi-layer PCBs are made from three or more double-layered PCBsstacked on top of every other. These boards are then glued along side a special adhesive and sandwiched between sheets of insulation to stop any of the components from melting. Multi- layer PCBs are available during a number of sizes, from four layers to 10 or twelve layers. 50 layers thick was the most important multi-layer PCB ever installed. Designers may create very dense, complex designs using several layers of computer circuit boards, which are ideal for a good sort of complicated electrical tasks. File servers, data storage, GPS technology, satellite systems, meteorology , and medical equipment are all applications where multi-layer PCBs are going to be helpful.

2. Existing system

The condition monitoring module's integrated circuits and designs allow designers to create applications with a high-speed signal chain, efficient processing, and a variety of wired and wireless interfaces to communicate to the outside world. Imagine if your car could tell you when one of its components, such as a belt, hose, spark plug, or wheel bearing, is about to fail. You could have parts replaced as needed before they wear out and be back on the road in a matter of minutes, similar to how you change a collection of tries today. As a result, you could save a lot of time and money on planned preventive maintenance, which involves replacing worn-out parts with new ones to prevent parts from failing while the machine is running. Scheduled maintenance, which requires leaving the car in the garage all day, may still be required on occasion, but only for a few parts and infrequently. You pay a lot of money for every minute the factory spends in the garage for repairs. You adhere to preventive maintenance plans to repair parts that might malfunction, however you wish there was a better way—a way to know exactly when something would fail and replace it just in time. Most replacements will need only minimal repairs and could be done at off-peak hours, avoiding the shutdown of an entire line or the factory as a whole. Predictive maintenance is the term for this process. By replacing or supplementing a conventional preventive maintenance programme with creative predictive maintenance, you can save money on components, labour, and factory downtime, and your investment can pay for itself several times over.



3. DESIGN FLOW

Fig 3.1

fig 3.2

4. Input power field

Over/under voltage or current protection, as well as miss-wiring protection, can be included in the input power protection. Diodes, fuses, and PTC have also been used to do this. However, as the need for security and efficiency grows, integrated circuit protection options such as E-fuse, Hotswap, and ideal diode are becoming more appealing.

4.1 Non isolated DC power supply

One or more front-end components (ADC, REF, amplifiers, power switches, etc.) on the field side of the module require power from the field supply. We have DC/DC converters, LDOs, and other devices for all power and voltage levels that can help minimize power dissipation and solution size.

4.2 Isolated DC power supply

The backplane is normally galvanically isolated from the field side (ore remote modules). Powerinput can be needed depending on whether the module has a field power link or a non-isolated power supply. Tiny and powerful fly-buck, fly-back, and push-pull topologies are all possible withour systems.

4.3 Input power protection

Over/under voltage or current protection, as well as miss-wiring protection, can be included in the input power protection. Diodes, fuses, and PTC have also been used to do this. However, as the need for security and efficiency grows, integrated circuit protection options such as E-fuse, Hotswap, and ideal diode are becoming more appealing.

Electromagnetic disturbances (EMC), surges, and quick transients events caused by the factoryenvironment can affect input and output signals to and from modules. IEC6100-4-x covers testing against such disturbances. To secure the various modules, we provide a wide range of ESD and TVS support. The vibration sensor input will provide power to the remote sensor and sample the sensor's vibration or acoustic information before sending it to the backplane. One ormore digital processing blocks, such as MPU, MCU, FPGA, or ASIC, are located on the backplaneside and must be controlled by the backplane voltage. DC/DC converters, LDOs, and power management ICs (PMICs) are all available to help minimise power dissipation and solution size.

5. Proposed system

Predictive maintenance detects, pre-processes, and reports on real-time conditions, communications must provide fast signalling and low latency, which are required in many applications by industry standards. The network must support wired and wireless sensor communications, as well as reporting to factory automation control centres, human operators through a human-machine interface, and the cloud. The standards used in today's industrial networks differ widely, and several protocols can be in use on the same factory floor. To make implementation easier, predictive maintenance communications should be consistent with more commonly used standards.

5.1 CC3220MODA

The totally programmable FCC, IC, CE, MIC, and SRRC confirmed remote microcontroller (MCU) module with worked in Wi-Fi network is an incredible spot to begin. The TI SimpleLink CC3220MODx andCC3220MODAx module family, intended for the Internet of Things, is a remote module that consolidates two truly isolated installed sensors MCUs on chips. An application processor—an ARM Cortex-M4 MCU with 256KB of RAM committed to the client and 1MB of Serial Flash as another option. Both Wi-Fi and web legitimate layers are controlled by an organization processor MCU. A 802.11b/g/n radio, baseband, and MAC with an incredible cryptomotor for simple, secure web associations with 256-cycle encryption are remembered for this ROM-based subsystem.

5.2 CC2652R

String, Zigbee®, Bluetooth® 5 low energy, IEEE 802.15.4g, IPv6-empowered keen items (6LoWPAN), Wi-SUN®, and exclusive frameworks are completely upheld by theCC2652R multiprotocol remote 2.4-GHz MCU. The CC2652R is essential for the CC26xx and CC13xx group of 2.4-GHz and Sub-1-GHz RF gadgets that are savvy and super low force. Low dynamicRF and microcontroller (MCU) flows, just as sub-A rest flows with up to 80KB of RAM maintenance, guarantee long battery life and permit activity with little coins-cells batteries.

5.3 ADS122U04

The ADS122U04 is a 24-bit exactness simple to-advanced converter (ADC) with many inherenthighlights that help limit gadget cost and part include in applications that action little sensor signals. Through an adaptable info multiplexer (MUX), the unit has two differential or four single

-finished data sources, a low-commotion, programmable addition speaker (PGA), two programmable excitation current sources, a voltage reference, an oscillator, and an accuracytemperature sensor, just as a voltage reference, an oscillator, and an exactness temperature sensor.

5.4 MSP432P4111

The Simple Link MSP432P411x and MSP432P401x microcontrollers (MCUs) are upgraded remote host MCUs with a coordinated 16-digit exactness ADC, conveying super low-power execution including 100 μ A/MHz in dynamic force and 820 nA in reserve power with FPU and DSP augmentations. The MSP432P411x and MSP432P401x are streamlined remote host MCUs that empower designers to give more-accuracy simple and memory augmentation to SimpleLink remote correspondence arrangements

5.5 HDC2080

In a little DFN box, the HDC2080 framework is an incorporated moistness and temperature sensor that furnishes high accuracy estimations with low force utilization. The capacitive-based sensor has new coordinated advanced highlights just as a warming component to disperse dampness and buildup. Programmable intrude on edges are remembered for the HDC2080 advanced highlights to give alerts and gadget wake-ups without the requirement for a microcontroller to constantly follow the framework. The HDC2080 highlights programmable inspecting stretches, low force utilization, and 1.8-V stock voltage support.

6. SCHEMATIC DIAGRAM

Overview of the condition monitoring module schematic diagram are been displayed below as (fig 6.1).

International Journal of Modern Agriculture, Volume 10, No.2, 2021 ISSN: 2305-7246



Fig 6.1 schematic diagram of condition monitoring module

7. OUTPUT

7.1 TOP ASSEMBLY DRAWING

The below figure is the Top Assembly drawing for the condition monitoring module. Theelectronic segments are fit onto the PCB utilizing an assortment of get together cycles, for example, surface mount method or a through-opening development. The segments utilized ought to likewise be viable with the get together interaction utilized.

International Journal of Modern Agriculture, Volume 10, No.2, 2021 ISSN: 2305-7246



Fig 7.1 Top assembly drawing 7.2 BOTTOM ASSEMBLY DIAGRAM

The below mentioned (fig 7.2) shows the Bottom Assembly diagram of condition monitoringmodule.



Fig 7.2 Bottom assembly diagram

PCB VIEWED FROM BOTTOM SIDE	BOARD #:	TIDA-010045	REV: E2	SUN REV: Not In Version(
	TID #:	TIDA-010045		
PLOT NAME = Bottom Assembly Drawing	GENERATE	D :	4:13:42 PM	

7.3 TOP AND BOTTOM OVERLAY

The below mentioned drawing shows the top overlay(fig 7.3) and bottom overlay (fig 7.4)of the Condition Monitoring Module.





Fig 7.3 top overlay

Fig 7.4 bottom overlay

8. Conclusion

This project is an example of Condition Monitoring Module Design, which is based on theroutine or permanent recording of machine condition, which is needed for predictive maintenance. They can be quickly alerted and secured in the event of any trouble. When the schematic procedures are finished, this project will be improved by using and creating gerber files, which include the top layer, bottom layer, trace layer, and trace width of the part for quick and accurate results.

REFERENCES

- S. A. Rikardo, C. B. Bambang, C. Sumaryadi, T. D. Yulian, S. E. Arief and S. F. Irvan Kharil, "Vibration monitoring on power transformer", 2008 International Conferenceon Condition Monitoring and Diagnosis, pp. 1015-1016, 2008.
- 2. C. B. Xu, F. H. Wang and Z. J. Jin, "Design and implementation of vibration on-line monitoring system of power transformer", 2013 IEEE Power & Energy SocietyGeneral Meeting, pp. 1-5, 2013.
- **3.** K. M. A. Kumar and T. C. Manjunath, "Vibration signal monitoring based on virtual instrumentation", 2017 IEEE International Conference on Power ControlSignals and Instrumentation Engineering (ICPCSI), pp. 1805-1807, 2017.
- 4. Q. R. Yan, X. Liu and J. G. Yin, "Features of Vibration Signal of Power Transformer Using the Wavelet Theory", High Voltage Engineering, vol. 33, no. 1, pp. 165-168, 2007.
- **5.** Y. Li and L. Liu, "The application of the wavelet packet to the study of vibration of transformer under DC magnetic bias", 2010 5th International Conference on Critical Infrastructure (CRIS), pp. 1-4, 2010.
- 6. H. J. Wang, Research on Transformer On-line Monitoring System Based on Vibration Signal North China Electric Power University, 2014.
- 7. C. H. Hu and G. H. Li, "Analysis and design of system based on MATLAB 6.X wavelet analysis" in , Xi'an:Xi'an Electronic Science & Technology University Press, pp. 23-26, 2004
- 8. H. Baba, T. Yang, M. Inoue, K. Tajima, T. Ukezono, and T. A. Sato, "A low power and small-area multiplier for accuracy-scalable approximate computing," in Proc. IEEE Comput. Soc. Annu. Symp. VLSI, 2018, pp. 569–574.
- I. Qiqieh, R. Shafik, G. Tarawneh, D. Sokolov, S. Das and A. Yakovlev, "Significance-driven logic compression for energy-efficient multiplier design," IEEE Trans. Emerg. Sel. Topics Circuits Syst., vol. 8, no. 3, pp. 417–430, Sep. 2018.
- 10. P. Pramod and T. K. Shahana, "Delay and energy efficient modular hybrid adder for signal processor architectures," IETE J. Res., Jun. 2, 2019.
- 11. N. V. V. K. Boppana, J. Kommareddy, and S. Ren, "Low-cost and highperformance8 x 8 booth multiplier," Circuits Syst. Signal Process., vol. 38,no. 9, pp. 4357–4368, 2019.
- 12. Sudhakar, K., Selvakumar, T. Jayasingh, T. "Design and implementation of adaptive clock gating technique with double edge triggered flip flops" ICIIECS 2015 2015 IEEE International Conference on Innovations in Information, Embedded and Communication Systems, 2015, 7193249
- 13. Arunprathap, S., Sudhakar, K. "Printed circuit board design of compact CAN to ethernet converter"International Journal of Scientific and Technology Research, 2020, 9(2), pp. 591–595
- Sakthimani, S., Kalaiarasan, R. "Investigationon analysis of power efficient 15/16 prescaler" International Journal of Scientific and Technology Research, 2020, 9(3), pp. 757-760.
 - 15. Dhamodaran, M., Jegadeesan, S., Murugan, A., Ramasubramanian, B. "Modeling and simulation of the flyback converter using SPICE model" International Journal of Recent Technology and Engineering, 2019, 8(3), pp. 946-952.